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Application of:

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Inventor:

Jong-Jan Lee, et al.

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Serial No.:

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Group #: 1763

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Title:

FABRICATION OF SILICON-ON-NOTHING (SON) MOSFET

FABRICATION USING SELECTIVE ETCHING OF Si_{1-x}Ge_x LAYER

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

ASSOCIATE POWER OF ATTORNEY

CHANGE OF CORRESPONDENCE ADDRESS

I hereby appoint Robert D. Varitz, of 4915 S.E. 33rd Place, Portland, Oregon 97202, U. S. Patent and Trademark Office Registration Number 31436, as an associate attorney for the above-captioned matter, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

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